

# nRF51822 specification for -25 to +85°C temperature range

Multiprotocol Bluetooth® 4.1 low energy/2.4 GHz RF SoC

# Specification v1.0

# Addendum to nRF51822 Product Specification v3.1

This addendum describes the deviations that apply when operating the nRF51822 chip in the -25 to  $+85^{\circ}$ C temperature range.

For information on the specifications when operating the nRF51822 chip in the Consumer Temperature Range (-25 to  $+75^{\circ}$ C), see the *nRF51822 Product Specification* v3.1.

This document applies to the following chip variants:

nRF51822 variant	Build code
QFAC	Ax0 <sup>1</sup>
CFAC	Ax0 <sup>1</sup>

1. The x in the build code can be any digit between 0..9.

**Table 1** nRF51822 variants qualified for the -25 to +85°C temperature range



## 1 Introduction

The nRF51822 chip variants mentioned in *Table 1* on page 1 has been qualified for operating in the -25 to +85°C temperature range.

This document specifies the performance when operating in the -25 to +85°C temperature range. The values in this document replace the corresponding values in the nRF51822 Product Specification v3.1 when operating outside the Consumer Temperature Range (-25 to +75°C) but inside the -25 to +85°C temperature range.



## 2 Deviations from the nRF51822 Product Specification v3.1

This chapter lists where there are differences from *nRF51822 Product Specification* v3.1, and the chapter or section name and number is referring to the chapter or section in the *nRF51822 Product Specification* v3.1. Changed values are written with bold font, and values or information that is not valid in the Industrial Temperature Range are written with strikethrough font.

#### Key features on the front page:

- Flexible power management
  - Supply voltage range 1.9 V to 3.6 V

#### 3.4.1 Power supply

nRF51 supports three different power supply alternatives:

- · Internal LDO setup
- DC/DC converter setup
- Low voltage mode setup

#### 3.4.1.3 Low voltage mode setup

Devices can be used in low voltage mode where a steady 1.8 V supply is available externally.

#### 7 Operating conditions

Changes in Table 20 (within the Consumer Temperature Range the specifications remains unchanged):

- New value for Min. voltage in the *Supply voltage*, *internal LDO setup* row.
- The row Supply voltage, low voltage mode setup is not valid.
- New values for Min. and Max. temperature in the *Operating temperature* row.

Symbol	Parameter	Notes	Min.	Тур.	Max.	Units
VDD	Supply voltage, internal LDO setup		1.9	3.0	3.6	V
VDD	Supply voltage, DC/DC converter setup		2.1	3.0	3.6	V
<del>VDD</del>	Supply voltage, low voltage mode- setup	1	<del>1.75</del>	1.8	<del>1.95</del>	¥
T <sub>A</sub>	Operating temperature		-25	25	85	°C

<sup>1.</sup> DEC2 shall be connected to VDD in this mode.

**Table 20** Operating conditions

#### 8.2 Power management

Footnote no. 1 on Table 32 is not valid for -25 to +85°C temperature range.



#### 8.5.4 Transmitter specifications

New value for Max. frequency in the 20 dB bandwidth for modulated carrier (250 kbps) row in Table 38 (within the Consumer Temperature Range the specifications remains unchanged):

Symbol	Description	Min.	Тур.	Max.	Units	Test level
P <sub>BW250</sub>	20 dB bandwidth for modulated carrier (250 kbps).		700	1000	kHz	2

**Table 38** Transmitter specifications

#### 8.12 Analog to Digital Converter (ADC) specifications

Changes in Table 51 (within the Consumer Temperature Range the specifications remains unchanged):

- New values for Min. and Max. in the Gain error row
- New values for Min. and Max. in the Internal Band Gap reference voltage error row
- References to 1.8 V are not valid for Industrial Temperature Range.

Symbol	Description	Note	Min.	Тур.	Max.	Units	Test level
$e_{G}$	Gain error.	1	-3		+3	%	2
V <sub>REF_VBG_ERR</sub>	Internal Band Gap reference voltage error.		-2		+2	%	2
ADC_ERR_1V8				3		LSB	2
ADC_ERR_2V2	Absolute error when used for			2		LSB	2
ADC_ERR_2V6	battery measurement at <del>1.8 V</del> , 2.2 V, 2.6 V, 3.0 V, and	2		1		LSB	2
ADC_ERR_3V0	3.4 V.			1		LSB	2
ADC_ERR_3V4				1		LSB	2

- 1. Source impedance less than 5 k $\Omega$ .
- 2. Internal reference, input from VDD/3, 10 bit mode.

Table 51 Analog to Digital Converter (ADC) specifications

#### 8.15 Temperature sensor (TEMP)

New values for Min. and Max. in the *Temperature sensor range* row in Table 54 (within the Consumer Temperature Range the specifications remains unchanged):

Symbol	Description	Note	Min.	Тур.	Max.	Units	Test level
T <sub>RANGE</sub>	Temperature sensor range.		-25		85	°C	N/A

**Table 54** Temperature sensor



## 11.5.2 QFAC QFN48 schematic with low voltage mode setup

Not valid for -25 to +85°C temperature range.

### 11.8.2 CFAC WLCSP schematic with low voltage mode setup

Not valid for -25 to  $+85^{\circ}$ C temperature range.



# 3 Typical characteristics

All plots are at VDD = 3 V, DC/DC disabled.

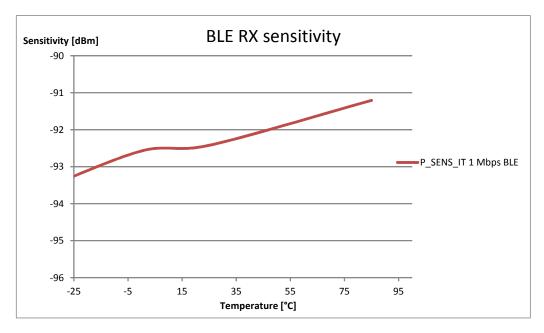


Figure 1 BLE RX sensitivity vs. temperature

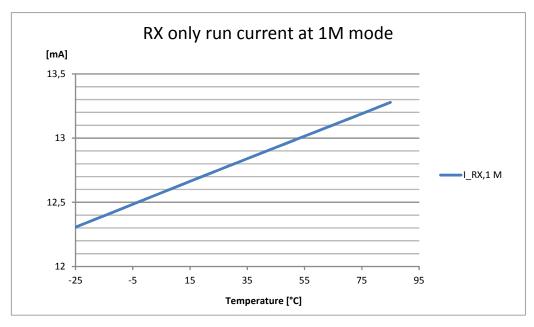
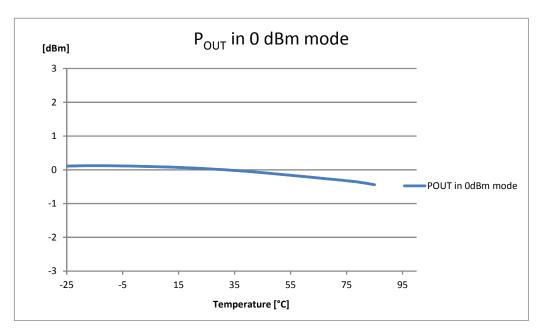
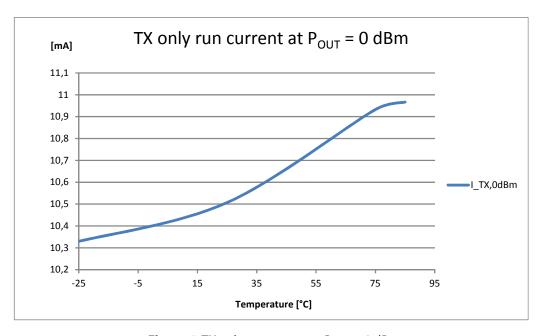


Figure 2 RX only run current at 1M mode





**Figure 3**  $P_{OUT}$  in 0 dBm mode.



**Figure 4** TX only run current at  $P_{OUT} = 0$  dBm.



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# **Revision History**

Date	Version	Description
February 2015	1.0	First release.