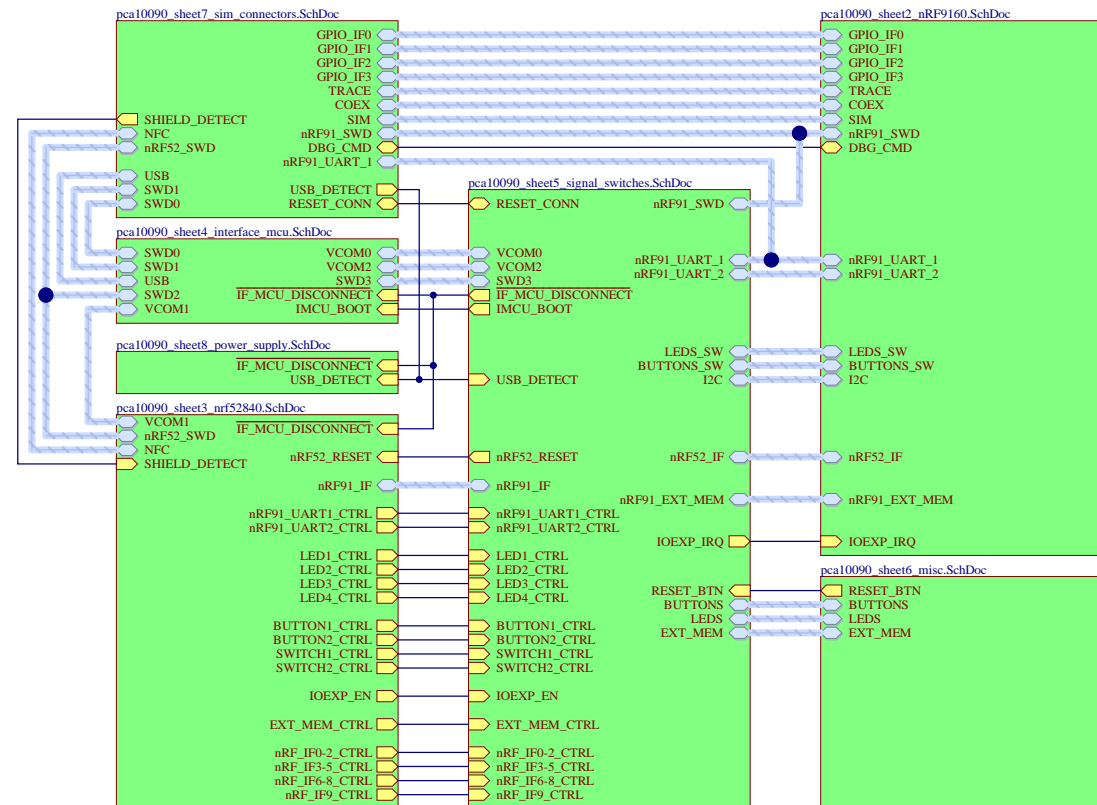



Nordic Semiconductor ASA

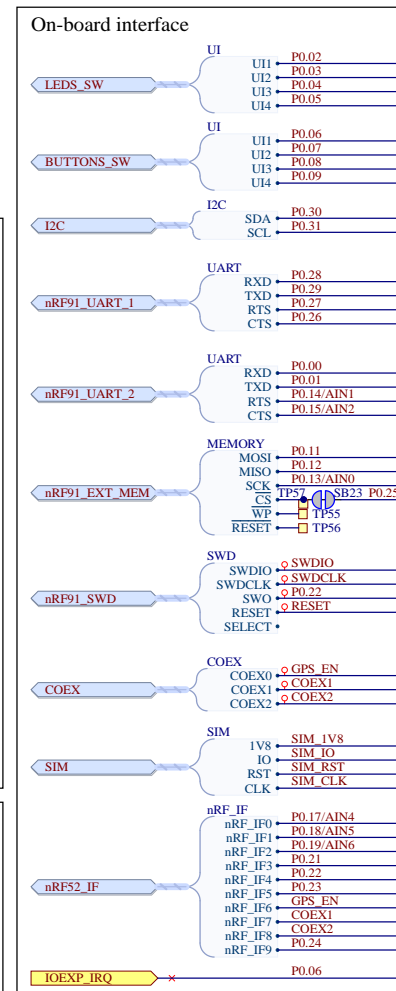
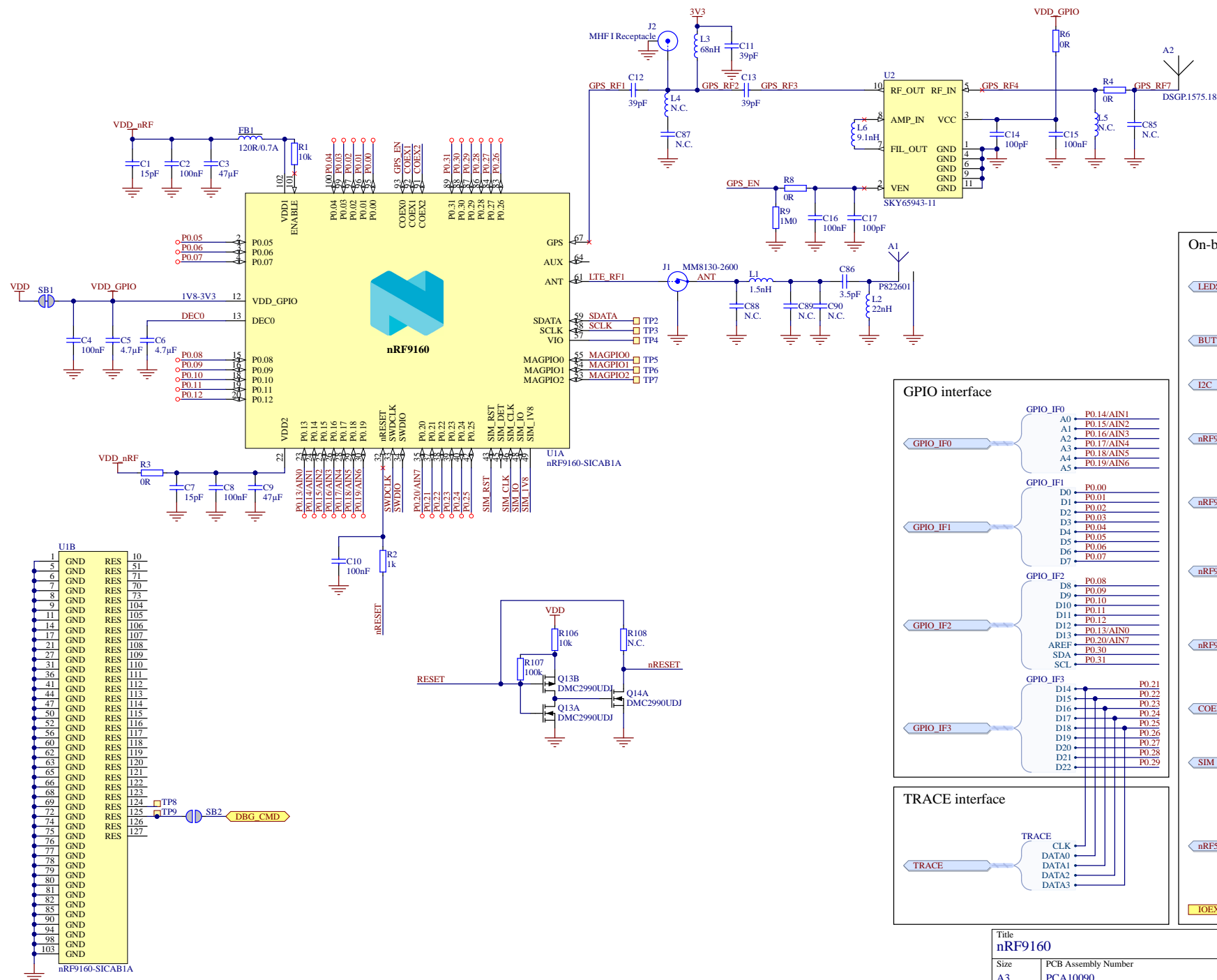
nRF9160 Development Kit (PCA10090)


Sheet 1:	Connections
Sheet 2:	nRF9160
Sheet 3:	nRF52840
Sheet 4:	Signal Switches
Sheet 5:	Interface MCU
Sheet 6:	Buttons and LEDs
Sheet 7:	SIM & Connectors
Sheet 8:	Power Supply

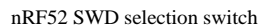



✖ The No ERC object is a design directive.
This directive is placed on a node in the circuit to suppress harmless warnings and/or error violation conditions that are detected when the schematic project is compiled.

Title		
Connections		
Size	PCB Assembly Number	
A3	PCA10090	Revision
		1.1.0
Date: 20.01.2022	Sheet 1 of 8	
File: pca10090_sheet1_connections.SchDoc	Drawn By: RUBR	

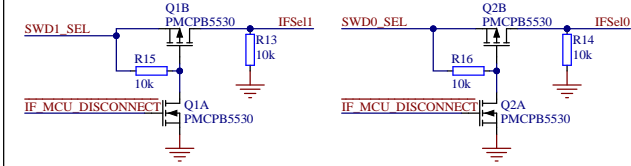


Title nRF9160		
Size A3	PCB Assembly Number PCA10090	
Revision 1.1.0		
Date: 20.01.2022		Sheet 2 of 8
File: pca10090_sheet2_nRF9160.SchDoc		Drawn By: RUBR

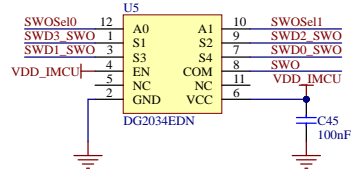


Title nRF52840		
Size A3	Revision 1.1.0	
Date: 20.01.2022 File: pca10090_sheet3_nrf52840.SchDoc		
		Sheet 3 of 8 Drawn By: RUBR

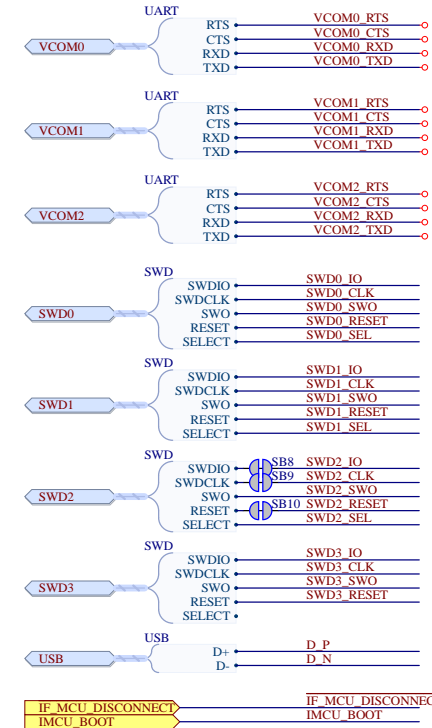
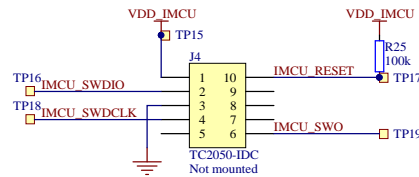
SWD0/I SEL reverse drive protection



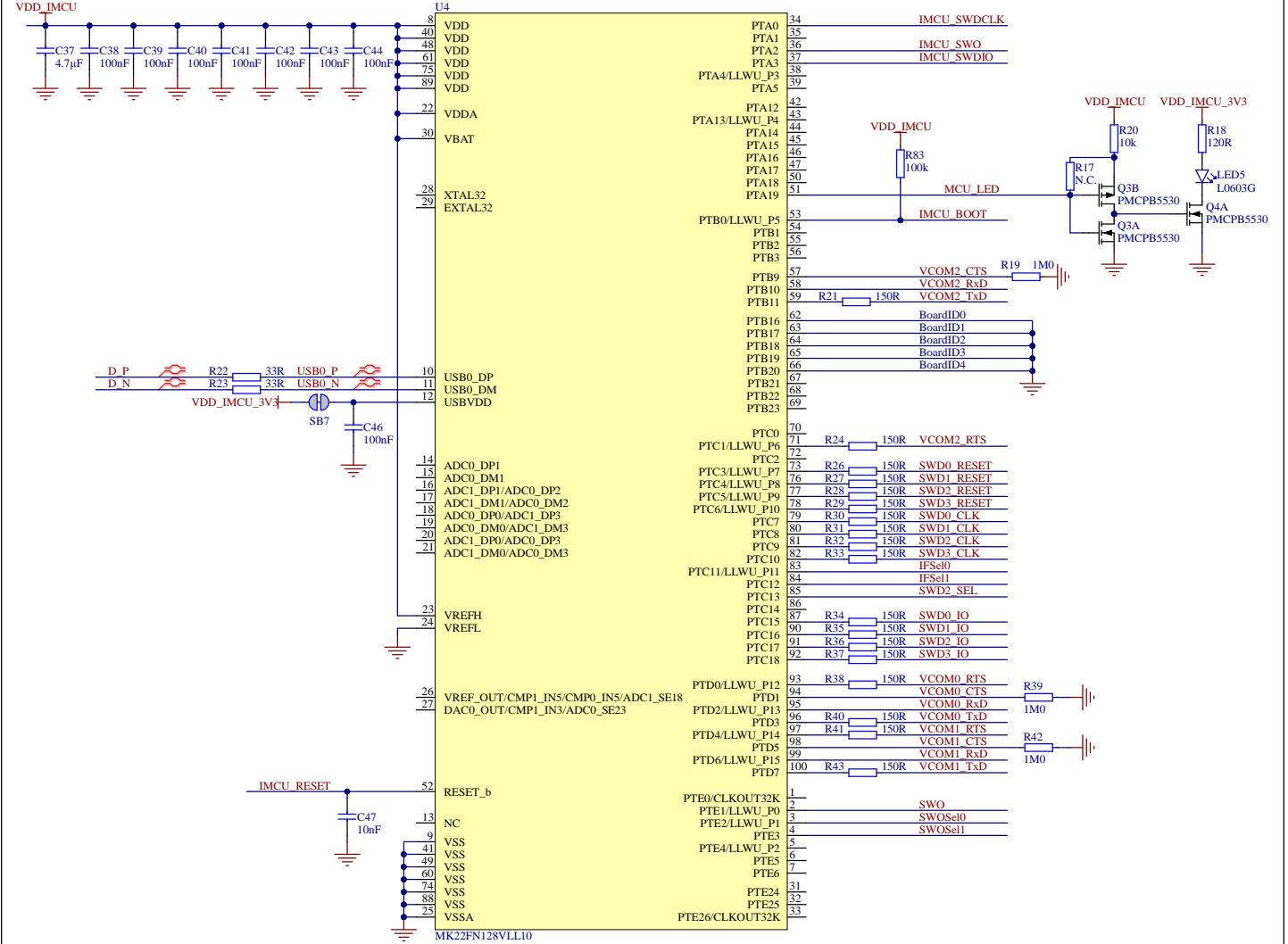
SWO MUX



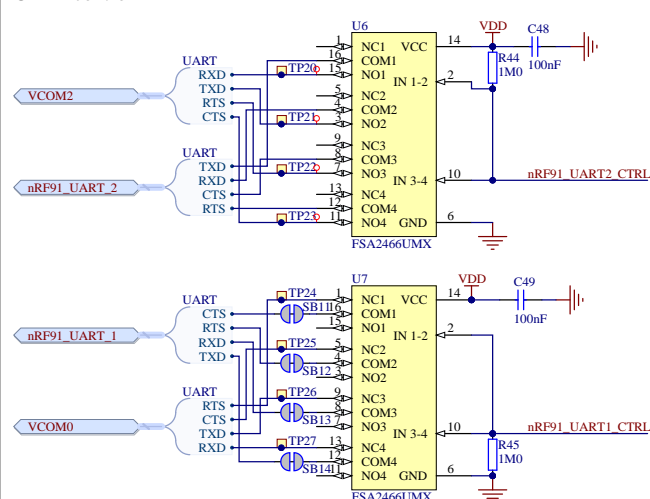
Interface MCU programming connector



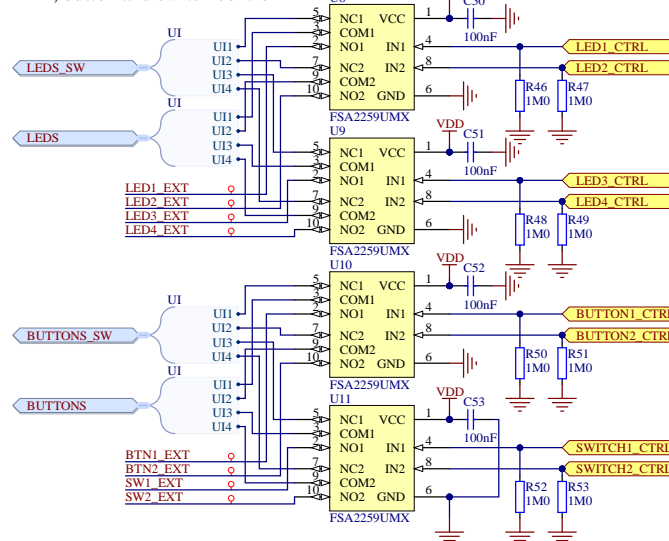
Interface MCU



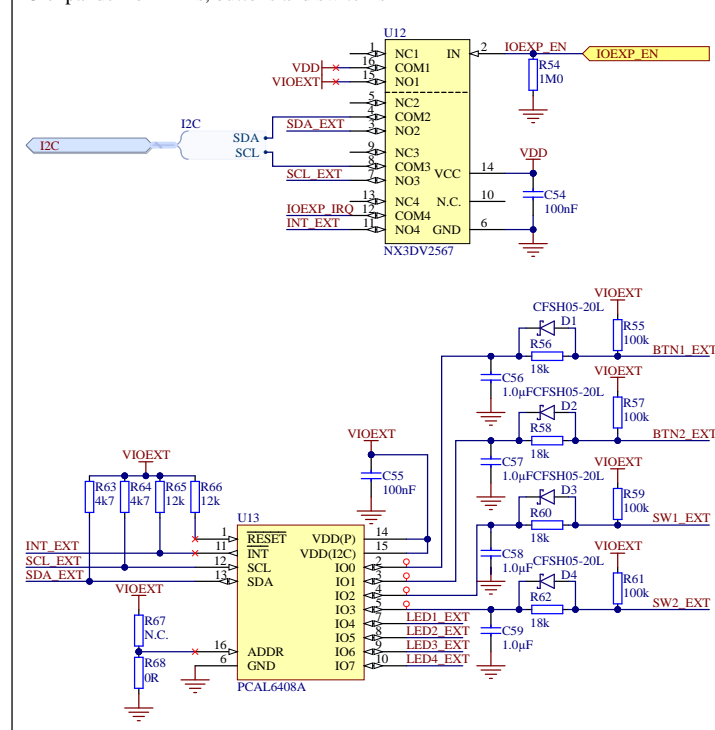
UART control



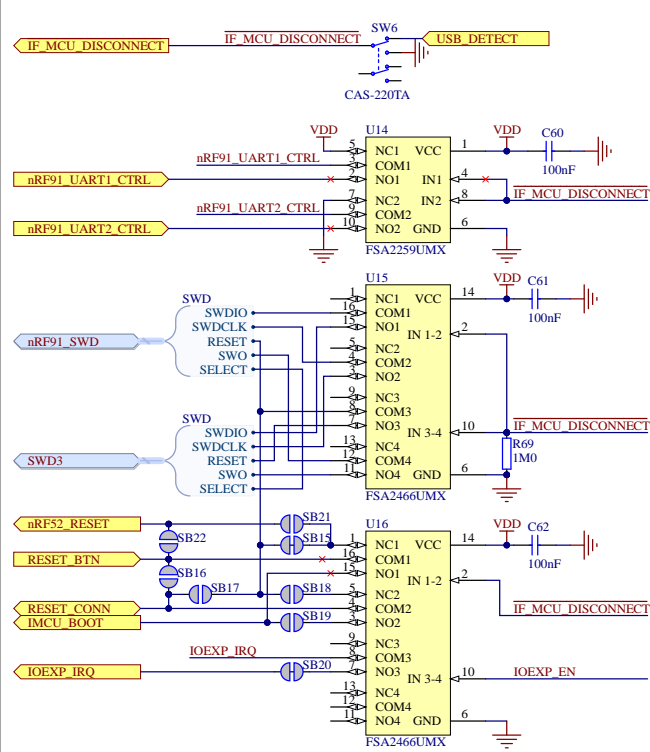
LED, button and switch control



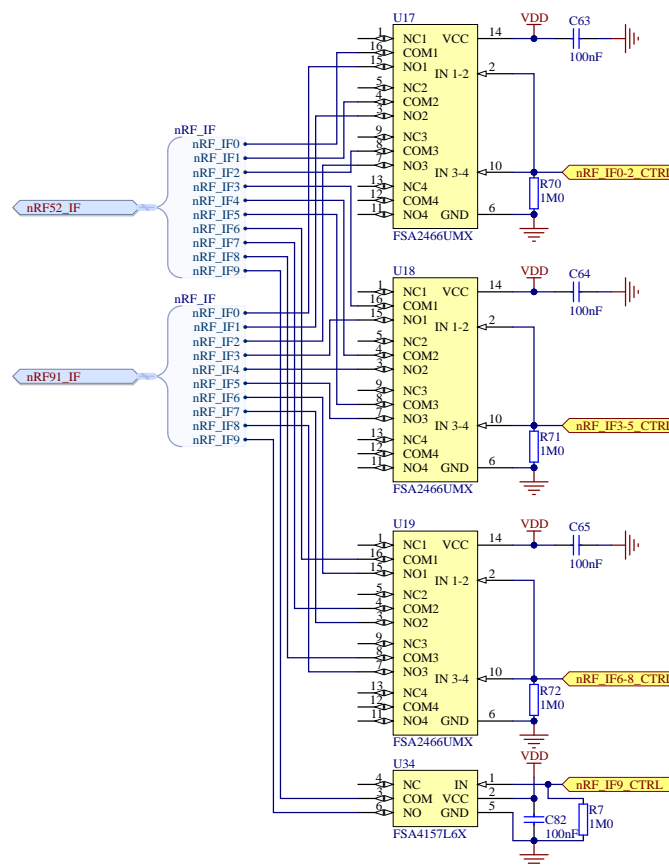
IO expander for LEDs, buttons and switches



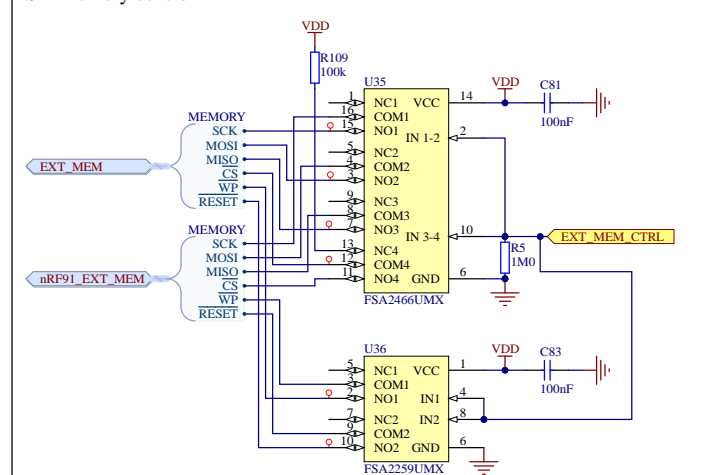
Interface MCU disconnect switches



nRF interface and COEX control

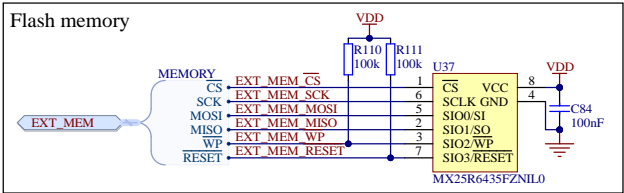
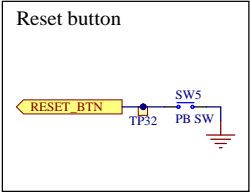
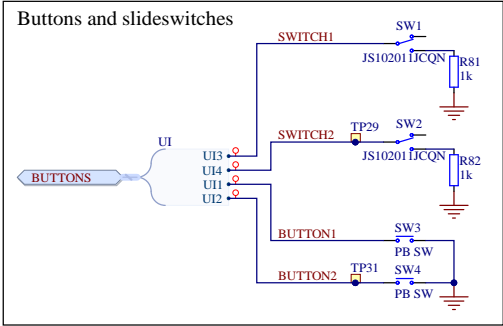
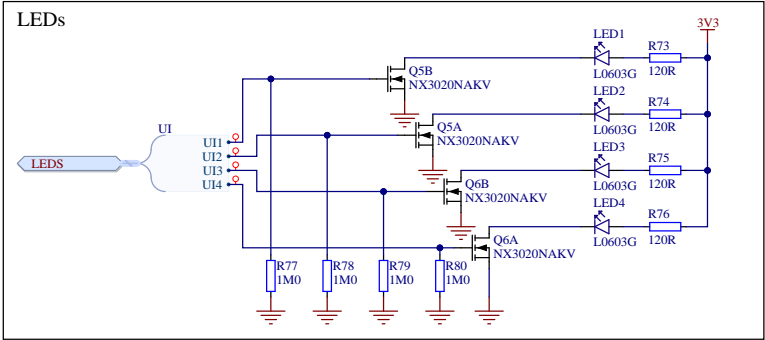


SPI memory control



Signal switches

Title	PCB Assembly Number	Revision	
Size	PCA10090	1.1.0	
Date: 20.01.2022	Sheet 5 of 8		
File: pca10090_sheet5_signal_switches.SchDoc	Drawn By: RUBR		



A



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Power sources and switches

The schematic diagram illustrates the power source and switch section of the ADI-PMU. It features an external supply (3.3 - 5.5V) connected to a pin header (P21) and a network of resistors (R89, R90, R91, R92, R93, R94), MOSFETs (Q8A, Q8B), and logic gates (U22, U23) to control the VBUS_EN and VIN3-5V_EN signals. The diagram also includes three ADI198ACFZ voltage monitors (U24, U25, U26) with their respective diodes (D7, D8, D9) and capacitors (C73, C74).

Buck regulator for VDD

VSUPPLY

D10

NSR0620P2T5G

C75

4.7µF

U27

VIN

CE/MODE

LX

VSS

VSS

FB

XC9236

L12

4.7µH

TP50

C76

100pF

C77

10µF

VREG

VFB

R100

120k

$V_{OUT} = 0.8 \times (R_{FB1} + R_{FB2}) / R_{FB2}$

R95

150k

R96

330k

SW9

Switch

VREG

3.0V / 1.8V (default) selection switch

3V3 supply

The diagram shows a 3V3 supply circuit. It starts with a VSUPPLY input connected to an SB32 Schottky diode. The diode's cathode is grounded, and its anode is connected to the Vin pin of a U28 LDO regulator (AP7333-33SAG-7). A 1.0μF capacitor (C78) is connected between the Vin pin and ground. The LDO's GND pin is also connected to ground. The LDO's Vout pin is connected to the 3V3 output, which is also connected to a 1.0μF capacitor (C79) to ground.

nRF91 current measurement

VSUPPLY SB33 VDD_nRF SB34 R97 N.C. VDD_nRF

P22
Pin List 1x2, Angled

Y1
Shunt 2.54mm
Placed on P22


Power switch

The diagram shows a power switch circuit. A switch is connected between $VSUPPLY$ and the gate of an NPN transistor $Q9$ (RV2C010UNT2L). The emitter of $Q9$ is grounded. The collector of $Q9$ is connected to VDD through a $10M$ resistor $R98$. A $1k$ resistor $R99$ is connected between VDD and the gate of $Q9$. The switch is also connected to a node labeled $TP49$. $TP48$ SW8 is connected to $VSENSE_SW_OUT$. $SB35$ is connected between VDD and VIO .

The schematic diagram illustrates the VIO_REF sense buffer and switch circuit. The VIO_REF input is connected to a sense buffer U29A (TS27L2IPT) through a feedback resistor R101 (100k). The sense buffer's output is connected to a switch Q10 (RV2C010UNT2L) through a resistor R102 (10M). The switch is controlled by VREG. The output of the switch is connected to a voltage monitor U32 (ADP198ACPZ) through a resistor R103 (1M). The voltage monitor U32 monitors VSENSE_SW_OUT. The circuit is powered by VSUPPLY and includes various passive components like capacitors C80 and C81, and resistors R104 and R105.

The diagram illustrates the MCU power switch interface. It features two ADP198ACPZ regulators, U30 and U31. U30 is connected to VDD and VDD_IMCU via SB36, and its EN pin is connected to the IF MCU DISCONNECT signal. U31 is connected to 3V3 and VDD_IMCU_3V3 via SB37, and its EN pin is also connected to the IF MCU DISCONNECT signal. Both regulators have VIN, VOUT, EN, SEL0, SEL1, and GND pins.

VSUPPLY & V5V interface

Title Power Supply		
Size A3	PCB Assembly Number PCA10090	
Revision 1.1.0		
Date: 20.01.2022		Sheet 8 of 8
File: pca10090_sheet8_power_supply.SchDoc		Drawn By: RUBR



