PCN no.: PCN-091 rev.1.0
Date: 2014-03-06
Device affected: nRF51822-CEAA

Device version / Build Code:
D00
D20

Data sheet references: N/A
Agreement reference: N/A
Customers reference: N/A

Description of change:
Introduction of ASE ChungLi (ASECL) as second source for wafer bump process for both the D00 (TSMC Fab 3, Deca Technologies) and D20 (TSMC Fab 10, Deca Technologies) versions. The two new versions - D10 (TSMC Fab 3, ASECL) and D30 (TSMC Fab 10 - ASECL) do not replace the current versions, but instead will be shipped in parallel with the current versions.

Impact: Does the change affect product:
1. Form ☒ No ☐ Yes – describe: 
2. Fit ☒ No ☐ Yes – describe: 
3. Function ☒ No ☐ Yes – describe: 
4. Quality or Reliability ☒ No ☐ Yes – describe:
Classification of change ☒ Minor ☐ Major

Reason for change:
Necessary and proactive action to ensure sufficient capacity to meet volume requirements

Consequences of change:
None.

Verification of change:
The new versions are qualified according to Nordic Semiconductor standard product approval and quality procedures.
Qualification reports and production samples are available now on request.

Marking/Shipping labels:
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<td><strong>Attachments:</strong></td>
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**Authorization for Nordic Semiconductor**

<table>
<thead>
<tr>
<th>Product Manager</th>
<th>Date: 2014-03-06</th>
<th>Sign:</th>
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<tbody>
<tr>
<td>Thomas Embla Bonnerud</td>
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<tr>
<th>Quality Director</th>
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<tbody>
<tr>
<td>Ebbe Romcke</td>
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